

TB\_Config.vhd

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   --  TB_RateGen.vhd will be simulated with this configuration
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5  --  When to simulate a design, you can assign one of your
   --  configuration names as a target design unit.
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10 configuration SIM_config of TestBench is
   for TB
   use work.all;
   for Master,Slave : PECL_Comp.SubCktRateGen
   use entity work.SubCktRateGen(SubCkt);
15   for SubCkt
   use work.all;
   for counter : PECL_Comp.E016 -- a name E016 is false
   use entity work.E016(SIM);
   end for;
20   end for;
   end for;
   end for;
   end SIM_config;

25 -----
   configuration NonSIM_config of TestBench is
   for TB
   use work.all;
   for Master,Slave : PECL_Comp.SubCktRateGen
30   use entity work.SubCktRateGen(SubCkt);
   for SubCkt
   use work.all;
   for counter : PECL_Comp.E016 -- a name E016 is false
   use entity work.E016(NonSIM);
35   end for;
   end for;
   end for;
   end NonSIM_config;
```